

“Mobile Wireless Communication Device      Exp. Mail No. EL 759669766 US  
Architectures And Methods Therefor”  
Atty. Docket No. CS11241

## ABSTRACT OF THE DISCLOSURE

A wireless communications architecture having first and second synchronous memory devices coupled to a virtual channel memory controller by corresponding first and second data buses, and a shared address and control bus interconnecting the virtual channel memory controller and the first and second synchronous memory devices. The first and second synchronous memory devices are addressed with the shared address bus, and the first and second memory locations are accessed via the first and second data buses, respectively.

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